



PG Certificate Programme in
VLSI Design

Course Highlights

- Well established courses with excellent placement records.
- 24-week full-time courses with 900 hours' theory + lab + project + 300 hours self study.
- 6-8 hours per day theory + lab sessions on 6 days a week in most centres.
- Continuous lab and internal assessments during the course.
- Centralised course-end theory exams across all centres.
- Courses designed and developed in consultation with the domain experts in C-DAC, academia and ICT industry.
- Expert faculty from C-DAC and ICT industry with widespread domain knowledge.
- Tutorials, hands-on and projects relevant to the standards of the ICT industry.
- Special training in aptitude, effective communication and interview skills.
- Extensive placement orientation and region-wise common campus placements.

Core Modules: Verilog, UVM, HDL Simulation, System Architecture, CMOS

PGCP-VLSI, a course derived from C-DAC's extensive research and development legacy, imparts knowledge in both theoretical and practical aspects of Very Large-Scale Integration design.

Course Modules

- Advanced Digital Design
- System Architecture
- Verilog HDL
- HDL Simulation and Synthesis
- System Verilog
- Verification using UVM
- Programming Fundamentals for Design and Verification, Linux Shell Scripting and Python
- CMOS VLSI and Aspects of ASIC Design
- Aptitude & Effective Communication
- Project



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